

**XYT-DAC\_QSFP+ to QSFP+**

RoHS Compliant 40Gb/s QSFP+ TO QSFP+ Copper Cable Assembly  
26 AWG PASSIVE

**Product Features**

- Support for multi-gigabit data rates up to 40Gbps
- Hot-pluggable QSFP+ footprint

- Available in lengths of 0.5, 1, 2, 3, 5 m
- Metal with lower EMI
- Low Power Consumption < 0.5W
- Power Supply :+3.3V
- Compatible to QSFP+ MSA
- Temperature Range: 0~ 70 ° C

## APPLICATIONS

- High capacity I/O in Storage Area Networks, Network Attached Storage, and Storage Servers
- Switched fabric I/O such as ultra high bandwidth switches and routers
- Data center cabling infrastructure
- High density connections between networking equipment

## STANDARD

- ESD to the Electrical PINs: compatible with MIL-STD-883E Method 3015.7
- RF Immunity compatible with IEC 61000-4-3
- EMI compatible with FCC Part 15 Class B EN55022 Class B
- RoHS compliant with 2002/95/EC 4.1&4.2 2005/747/EC

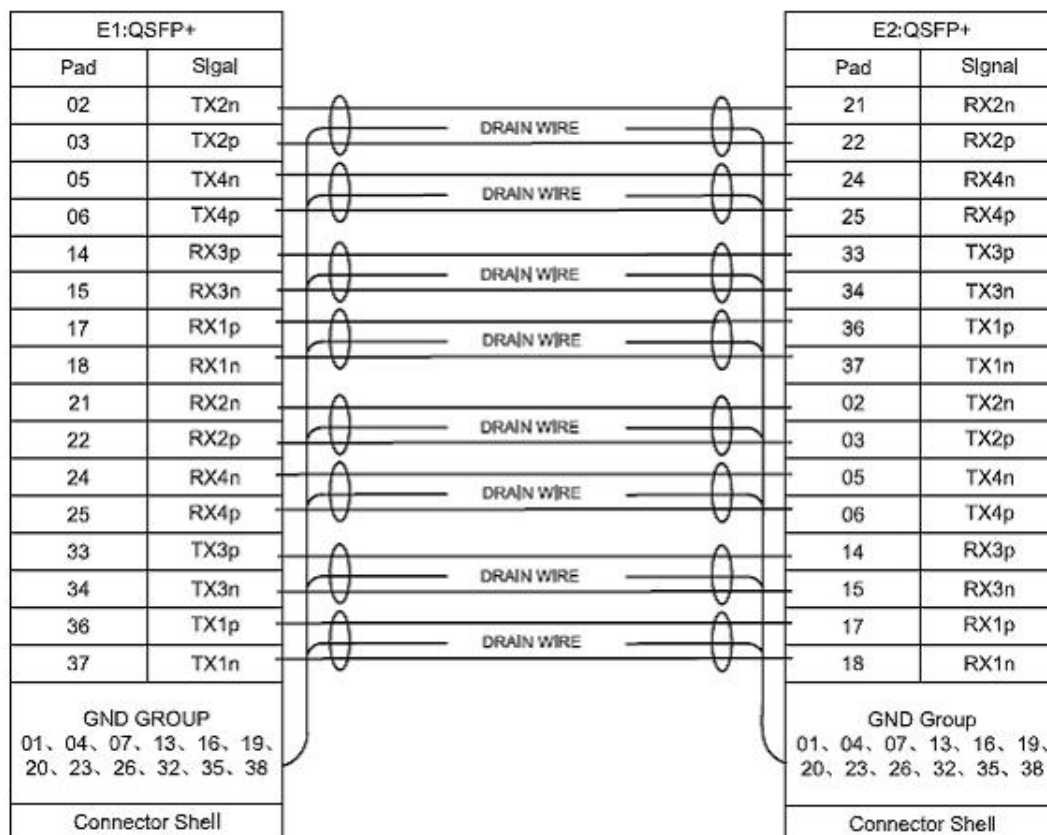
## Product Description

The QSFP+ to QSFP+ Passive cable assemblies are high performance, cost effective QSFP+ equipment interconnects . The Hybrid cables are compliant with SFF-8436 and SFF-8431 specifications. It is offer a low power consumption, short reach interconnect applications. providing an aggregated rate of 40Gb/s.

## Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Storage Ambient Temperature		-10		+85	°C
Operating Case Temperature	Tc	0		70	°C
Power Supply Voltage	VCC3	3.14	3.3	3.47	V
Power Dissipation	PD			0.02	W

### Cable Connection:



### QSFP+ Pin Descriptions

Pin	Logic	Symbol	Name/Description	Notes
1		GND	Ground	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input	
4		GND	Ground	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input	
7		GND	Ground	1
8	LVTTL-I	ModSelL	Module Select	
9	LVTTL-I	ResetL	Module Reset	
10		Vcc Rx	+3.3V Power Supply Receiver	2
11	LVCMOSI/O	SCL	2-wire serial interface clock	
12	LVCMOSI/O	SDA	2-wire serial interface data	
13		GND	Ground	1
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	
15	CML-O	Rx3n	Receiver Inverted Data Output	

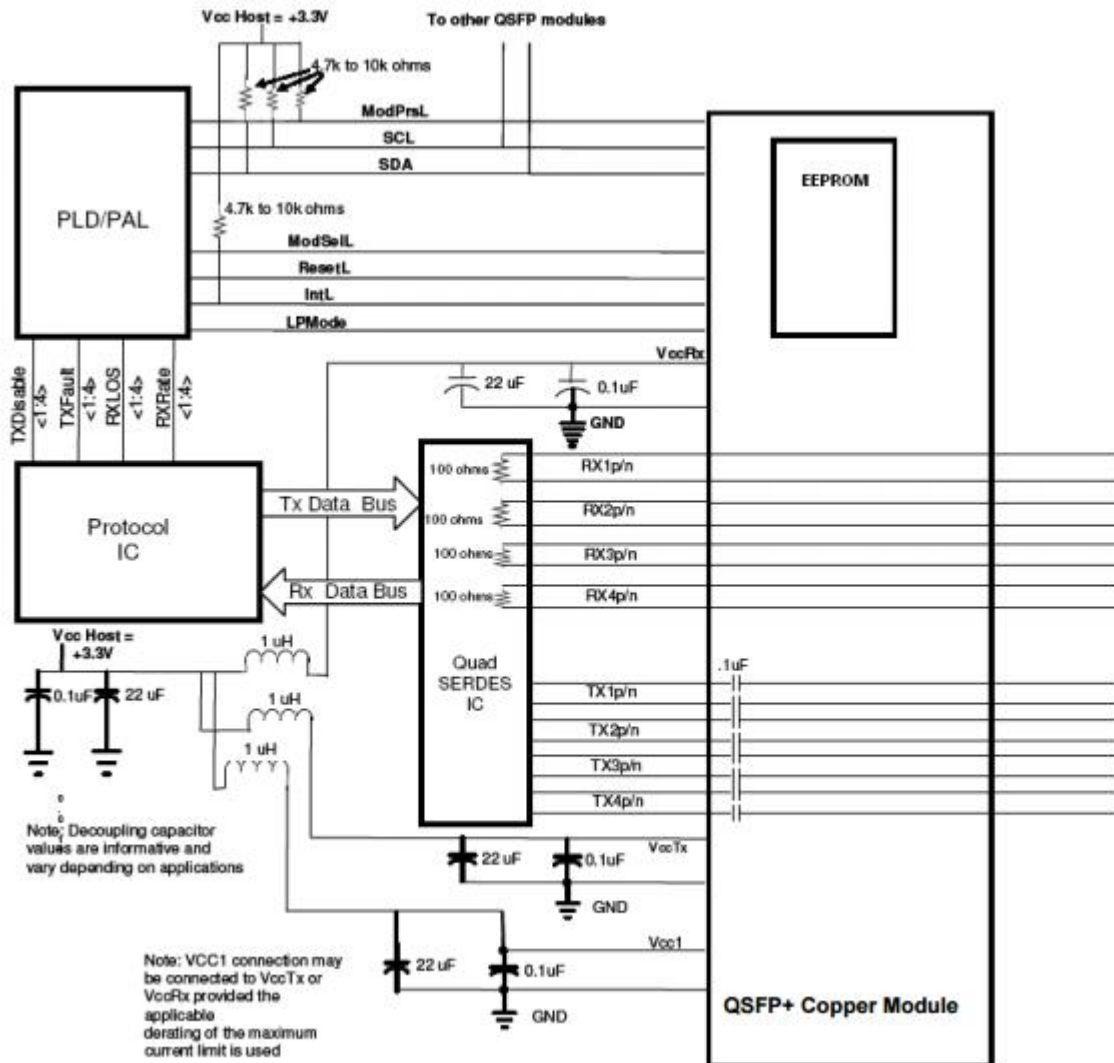
16		GND	Ground	1
17	CML-O	Rx1p	Receiver Inverted Data Output	
18	CML-O	Rx1n	Receiver Non-Inverted Data Output	
19		GND	Ground	1
20		GND	Ground	1
21	CML-O	Rx2n	Receiver Inverted Data Output	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	
23		GND	Ground	1
24	CML-O	Rx4n	Receiver Inverted Data Output	
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	
26		GND	Ground	1
27	LVTTL-O	ModPrsL	Module Present	
28	LVTTL-O	IntL	Interrupt	
29		Vcc Tx	+3.3V Power supply transmitter	
30		Vcc1	+3.3V Power supply	2
31	LVTTL-I	LPMODE	Low Power Mode	
32		GND	Ground	1
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	
34	CML-I	Tx3n	Transmitter Inverted Data Input	
35		GND	Ground	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	
37	CML-I	Tx1n	Transmitter Inverted Data Input	
38		GND	Ground	1

**Notes:**

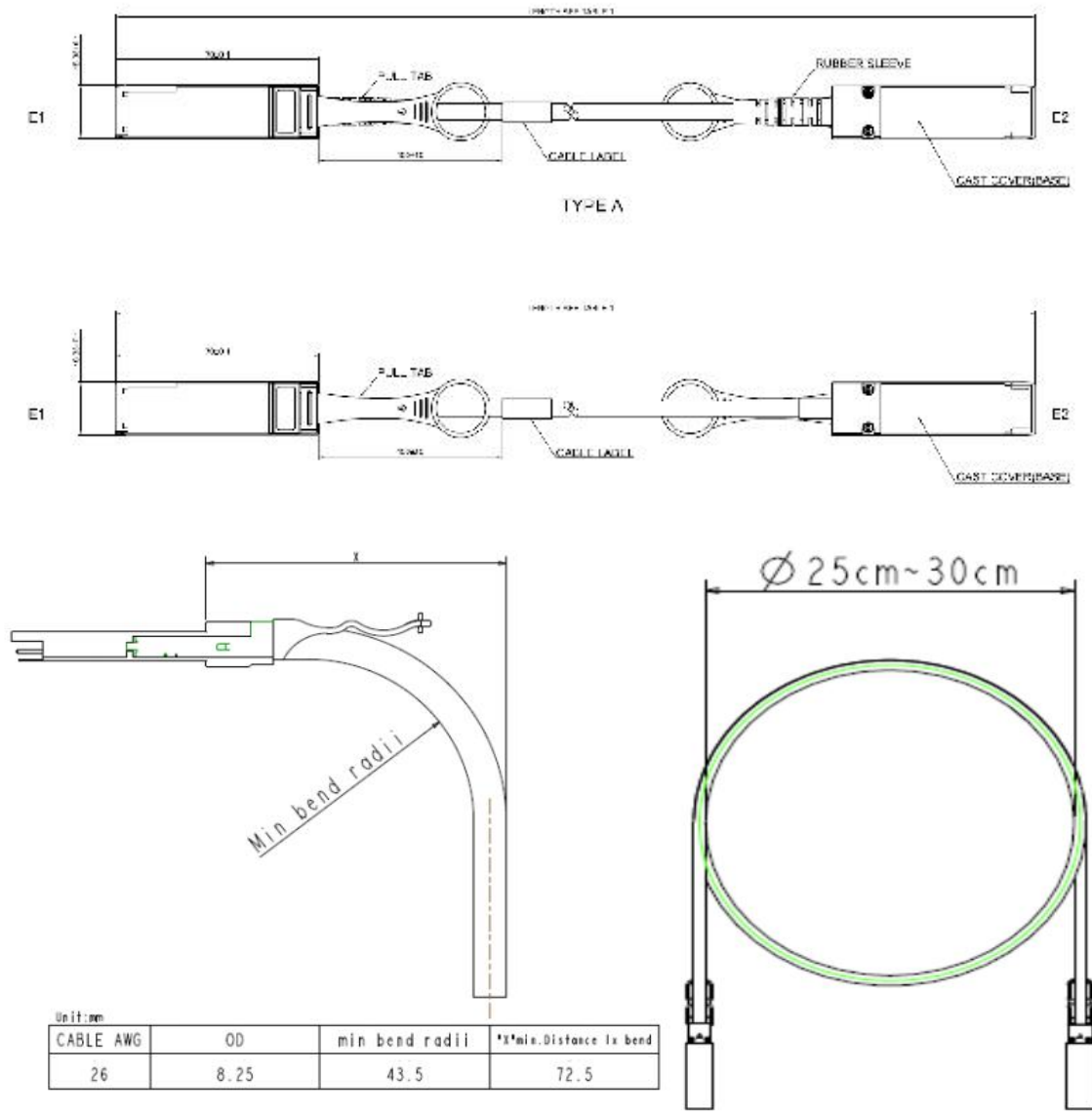
1: GND is the symbol for signal and supply (power) common for the QSFP+ module. All are common within the QSFP+ module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal-common ground plane.

2: Vcc Rx, Vcc1 and Vcc Tx are the receiver and transmitter power supplies and shall be applied concurrently. Requirements defined for the host side of the Host Edge Card Connector are listed in Table 6. Recommended host board power supply filtering is shown in Figure 4. Vcc Rx Vcc1 and Vcc Tx may be internally connected within the QSFP+ Module module in any combination. The connector pins are each rated for a maximum current of 500 mA.

# QSFP+ Host Board Schematic for active cable



### Mechanical Dimensions



### Ordering Information & Related Products

Part	Description
XYT-DAC_SFP+ to	QSFP+ To QSFP+ Direct Attach Cables, 1m (26AWG),0°C ~ +70°C
XYT-DAC_SFP+ to	QSFP+ To QSFP+ Direct Attach Cables, 3m (26AWG),0°C ~ +70°C
XYT-DAC_SFP+ to	QSFP+ To QSFP+ Direct Attach Cables, 5m (26AWG),0°C ~ +70°C